

December 2012

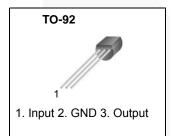
KA75330 Voltage Detector

Features

- · Detects Against Error Operations at Power On/Off
- · Resets Function for Low-Voltage Microprocessor
- · Checks Low-Battery

Description

The KA75330 prevents the system error where the supply voltage is below the normal level at the time when the power is on and instantaneous power off in systems.



Ordering Information

Part Number	Operating Temperature Range	Top Mark	Package	Packing Method
KA75330ZTA	-25 ~ +85°C	KA75330	TO-92	Ammo

Block Diagram

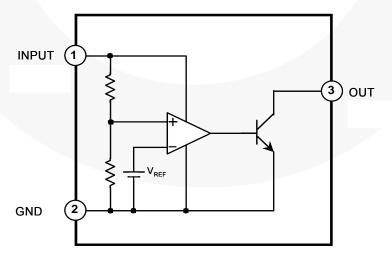


Figure 1. Block Diagram

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Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	0.3 ~ +15.0	V
V _{DET}	Detecting Voltage	3.3	V
V _{HYS}	Hysteresis Voltage	50	mV
T _{OPR}	Operating Temperature	-25 ~ +85	°C
T _{STG}	Storage Temperature	-50 ~ +150	°C
P_{D}	Power Dissipation (TO-92)	200	mW
DV _{DET} /DT	Detecting Voltage Temperature Coefficient	$R_L = 200 \Omega, +0.01$	%/°C

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
V _{DET}	Detecting Voltage		R_L = 200 Ω, $V_{OL} \le 0.4 \text{ V}$	3.15	3.30	3.45	V
V _{OL}	Low Output Voltage		R _L = 200 Ω			0.4	V
I _{LKG}	Output Leakage Current		V _{CC} = 15 V			0.1	μΑ
V _{HYS}	Hysteresis Voltage		R _L = 200 Ω	30	50	100	mV
DV _{DET} /DT	Detecting Voltage Temperature Coefficient		R _L = 200 Ω		±0.01		%/°C
I _{CCL}	Circuit Current (At On Time)		$V_{CC} = V_{DET(MIN)} - 0.05 V$		300	500	μΑ
I _{CCH}	Circuit Current (At Off Time)	10.00	V _{CC} = 5.25 V		30	50	μΑ
V _{TH(OPR)}	Threshold Operating Voltage		R_L = 200 Ω, $V_{OL} \le 0.4 \text{ V}$		0.8	1.0	V
t _{OL}	" L" ± Transmission Delay		$R_L = 1.0 \text{ k}\Omega, C_L = 100 \text{ pF}$	0.6	10		μs
t _{OH}	" H" ± Transmission Delay		$R_L = 1.0 \text{ k}\Omega, C_L = 100 \text{ pF}$		15	20	μs
I _{OLI}	Output Current (At On Time)		$V_{CC} = V_{DET(MIN)} - 0.05 V,$ $T_A = 25^{\circ}C$	10	20	30	mA
I _{OLII}	Output Current (At On Time)		$V_{CC} = V_{DET(MIN)} - 0.05 V,$ $T_A = -25 \sim +85^{\circ}C$	8	16	30	mA

Test Circuit 1.

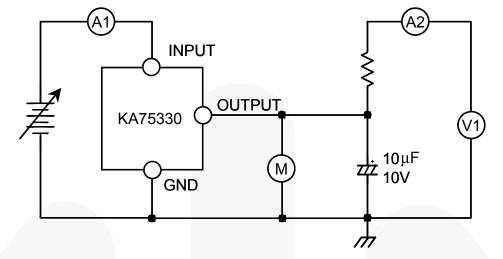


Figure 2. Test Circuit

Test Circuit 2.

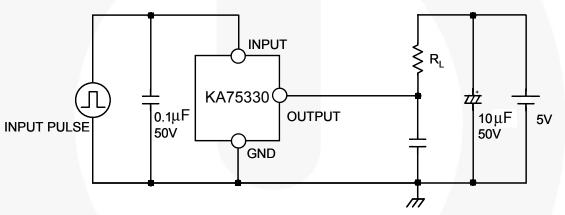


Figure 3. Test Circuit

Test Circuit 3.

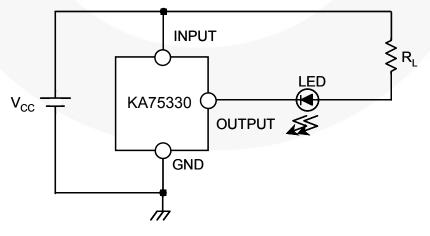


Figure 4. Test Circuit

Application Circuit

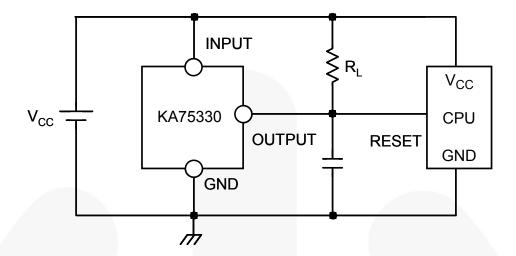


Figure 5. Application Circuit

Physical Dimensions

TO-92 Ammo Type

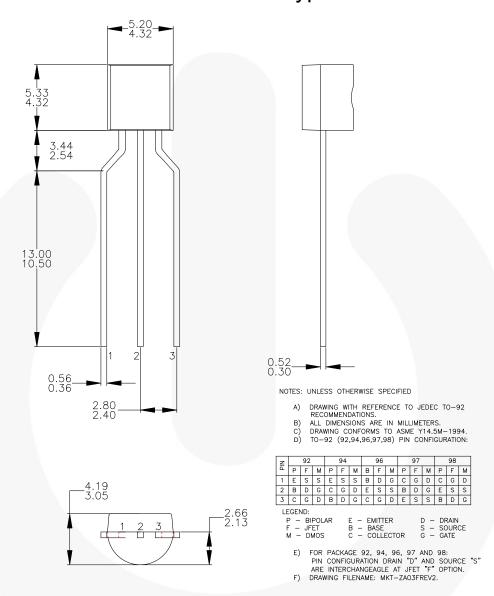


Figure 6. 3-Lead, TO-92, Molded, 0.200 in Line Spacing Lead Form

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Definition of Terms				
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